

Appl. No. 10/721,310  
Amdt. Dated April 26, 2007  
Reply to Final Office Action of February 2, 2007

Attorney Docket No. 81788.0261  
Customer No.: 26021



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Go Iwasaki

Serial No: 10/721,310

Confirmation No.: 7084

Filed: November 25, 2003

For: OUTPUT BUFFER CIRCUIT AND  
SEMICONDUCTOR MEMORY  
USING THE SAME

Art Unit: 2827

Examiner: Thong Quoc Le

I hereby certify that this correspondence  
is being deposited with the United States  
Postal Service with sufficient postage as  
first class mail in an envelope addressed  
to: Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

APR 26, 2007  
Date of Deposit  
William H. Wright, Reg. No. 36,312  
Name  
Signature  
04/26/07  
Date

**AMENDMENT AND RESPONSE**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This is in response to the Final Office Action dated February 2, 2007. Please  
amend the above-referenced application as follows:

**Amendments to the claims** are identified in list starting on page 2 of this  
paper.

**Remarks** begin on page 6 of this paper.